DIRECTION (Controller)	SIGNAL FUNCTION	SIGNAL NAME	P1 PIN NO. (mFD)	CHID	
	Ground		1		
to	Read data clock composite	RDA-N	2	30	RDA
	Ground		3		
from	Load Heads 1	HLD1-N	4		
	Ground		5		
to	Track 00	TRO-N	6	26	TRKO
	Ground		7		
to	Index	IND-N	8		
	Ground		9		
from	Load Heads 2	HLD2-N	10		
	Ground		11		
from	Step	STP-N	12	20	STEP
	Ground		13		
from	Direction	DIR-N	14	18	DIRS
	Ground		15		
from	Write enable	WREN-N	16	24	WRG
	Ground		17		
from	Write data	WRDA-N	18	22	WDAT
	Ground		19		
from	Unit Select 1	US1-N	20	10	DRS1
	Ground		21		
from	Unit Select 2	US2-N	22	12	DRS2
	Ground		23		
from	Motor-on 2	MTRN2-N	24 **		
	Ground		25		
from	Motor-on 1	MTRN1-N	26 **	16	MONn
	Ground		27		
to	Unit Ready Status 1	URDY1-N	28		
	Ground		29		
to	Unit Ready Status 2	URDY2-N	30		
	Ground		31		
to	Disk in Door cl 1	(DC1-N) DIDC1-N	32 *		
	Ground		33		
to	Disk in door cl 2	(DC2-N) DIDC2-N	34 *		
	Ground		35		
to	Write Protect	WRP-N	36	28	WRP
	Ground		37		
	to be terminated		38		
	Ground		39		
from	Head Select	HDS-N	40	32	SID2
	Ground		41		
from	Door Unlock 1	DUN1-N	42		
	Ground		43		
	not connected		44		
	Ground		45		
	to be terminated		46		
	Ground		47		
from	Door Unlock 2	DUN2-N	48		
	Ground		49		
	not connected		50		

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GROUND

* See note 2 at end of section 1.4.2.
** See note 3 at end of section 1.4.2.

Figure 1.3 X3121, X3122 INTERFACE CONNECTIONS